| STATEMENT UNDER 37 CFR 3.73(b) | |
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| Applicant/Patent Owner: Sang Sic YOON | |
| Application No./Patent No.: 10/608,569 / 6,842,060 Filed/Issue | Date: June 30, 2003 / January 11, 2005 |
| Entitled: DIGITAL CONTROL LOGIC CIRCUIT HAVING A CHARACTERISTIC OF TIME HYSTERESIS | |
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| in the United States Patent and Trademark Office at Reel <u>014247</u> , Frame <u>0383</u> , or a true copy of the original assignment is attached. OR B. A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as follows: | |
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| As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11. [NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.081 | |
| The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee. | |
| /Steve Y. Cho/ | 09-06-07 |
| Signature | Date |
| Steve Y. Cho | 206-467-9600 |
| Printed or Typed Name | Telephone Number |
| Attorney of Record, Reg. No. 44,612 | |
| Title | |